DECEMBER 1972 REVISED DECEMBER 1983

### description

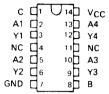
Operation of these monolithic 4-bit true/complement elements is controlled by the B and C inputs. With the B input low, the 4-bit binary input (A) is transferred to the output (Y) in either complementary form (with C low) or true form (with C high). When the B input is high, the output will be at the complementary level of the C input regardless of the levels of the data inputs.

These circuits are fully compatible for use with other TTL circuits. Input clamping diodes are provided to minimize transmission line effects and thereby simplify system design. Each input represents only one normalized series 54H/74H load, and full fan-out to 10 series 54H/74H loads is available from each of the outputs in the low-level condition.

Power dissipation is 270 mW typically with an average propagation delay of 14 ns from data inputs to output.

The SN54H87 is characterized for operation over the full military temperature range of 55°C to 125°C, and the SN74H87 is characterized for operation from 0"C to 70"C.

SN54H87 . . . J OR W PACKAGE SN74H87 . . . J OR N PACKAGE (TOP VIEW)



NC No internal connection

### **FUNCTION TABLE**

	TROL UTS	OUTPUTS				
В	С	Y1	Y2	Y3	Y4	
L	L	Ā1	Ã2	A3	A4	
L	н	A1	A2	А3	A4	
н	L	н	Н	Н	н	
н	н	L	L	L	L	

high level, L. low level

A1, A2, A3, A4 the level of the respective A input.

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V <sub>CC</sub> (see Note 1)			. 7 V
Input voltage			. 5.5 V
Operating free-air temperature range:	SN54H87 Circuits		to 125°C
	SN74H87 Circuits		to 70°C
Storage temperature range		-65°C 1	to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.

### recommended operating conditions

		SN54H87			SN74H87		
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Supply voltage, V <sub>CC</sub>	4.5	5	5.5	4.75	5	5.25	v
High-level output current, IOH			1			1	mA
Low-level output current, IOL			20		-	20	mA
Operating free-air temperature, TA	55		125	0		70	"C

TTL DEVICES

PRODUCTION DATA

This document contains information current as of publication date. Products conform to a section of the publication date of the section of the sec



## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP‡	MAX	UNIT
ViH	High-level input voltage		2			٧
VIL	Low-level input voltage				0.8	٧
VIK	Input clamp voltage	V <sub>CC</sub> = MIN, I <sub>I</sub> = -8 mA			-1.5	٧
Voн	High-level output voltage	$V_{CC} = MIN$ , $V_{IH} = 2 V$ , $V_{IL} = 0.8 V$ , $I_{OH} = -1 m$	2.4	3.5		v
VOL	Low-level output voltage	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V, V <sub>IL</sub> = 0.8 V, I <sub>OL</sub> = 20 m	А	0.2	0.4	٧
Ti	Input current at maximum input voltage	V <sub>CC</sub> = MAX, V <sub>I</sub> = 5.5 V			1	mA
<del>і</del> н	High-level input current	V <sub>CC</sub> = MAX, V <sub>1</sub> = 2.4 V			50	μА
1 <sub>1</sub> L	Low-level input current	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0.4 V			-2	mA
los	Short-circuit output current §	V <sub>CC</sub> = MAX	-40		-100	mA
		V <sub>CC</sub> = MAX, SN54H	87	54	78	
Icc	Supply current	See Note 2 SN74H	87	54	89	mA.

<sup>†</sup>For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type.

NOTE 2: ICC is measured for the following conditions:

- a. All A inputs are at 4.5 V, B and C inputs are grounded, and all outputs are open.
- b. B and C inputs are at 4.5 V, all A inputs are grounded, and all outputs are open.

### switching characteristics, VCC = 5 V, TA = 25°C

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	MAX
	Propagation delay time, low-to-high-			14	20	ns
tPLH	level output from any A input					
	Propagation delay time, high-to-low-		1	13	19	ns
tPHL.	level output from any A input	CL = 25 pF, RL = 280 $\Omega$ ,				
tPLH	Propagation delay time, low-to-high-	See Note 3	-	17	25	ns
	level output from B or C inputs					
	Propagation delay time, high-to-low-			17	25	ns
†PHL	level output from B or C inputs					

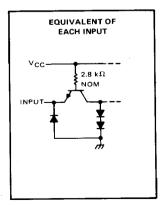
NOTE 3: See General Information Section for load circuits and voltage waveforms.

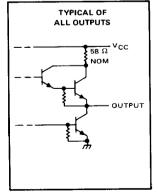
### logic diagram

# A1 (2) (3) Y1 A2 (5) (6) Y2 A3 (10) (9) Y3 B (8) (12) Y4

Pin numbers shown on logic notation are for J or N packages

### schematics of inputs and outputs





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 $<sup>\</sup>ddagger$ All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C. \$Not more than one output should be shorted at a time and duration of the short-circuit should not exceed 1 second.

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# **Texas Instruments**

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